

---

# EmETXe-i89M0

**COM Express® Basic  
Type 6 CPU Module**

## **User's Manual** Version 1.0

---

## Revision History

Version	Date	Description
1.0	Nov, 2016	Initial release

---

<b>Preface</b>	<b>iii</b>
Copyright Notice.....	iii
Declaration of Conformity .....	iii
CE.....	iii
FCC Class B .....	iii
RoHS.....	iv
SVHC / REACH.....	iv
Warning .....	v
Replacing the Lithium Battery.....	v
Technical Support .....	v
Warranty .....	vi
<b>Chapter 1 - Introduction</b>	<b>1</b>
1.1 The Product.....	2
1.2 About This Manual.....	2
1.3 Specifications .....	3
1.4 Inside the Package .....	4
1.5 Ordering Information.....	4
1.5.1 Optional Accessories.....	5
1.6 The Installation Paths of CD Driver .....	5
<b>Chapter 2 - Board Overview</b>	<b>7</b>
2.1 What Is “COM Express®”?	8
2.2 Board Dimensions.....	9
2.3 Block Diagram .....	10
2.4 Connector Pin Definition .....	11
<b>Chapter 3 - Installation &amp; Maintenance</b>	<b>15</b>
3.1 Installing the CPU Module on Carrier Board.....	16
3.2 Installing the Heatsink .....	17

<b>Chapter 4 - BIOS</b>	<b>19</b>
4.1 Main .....	20
4.2 Advanced.....	22
4.2.1 CPU Configuration .....	23
4.2.2 PCI Subsystem Settings .....	25
4.2.3 SATA Configuration.....	26
4.2.4 ACPI Settings.....	27
4.2.5 USB Configuration .....	28
4.2.6 AMT Configuration .....	30
4.2.7 Hardware Monitor.....	31
4.2.8 Super IO Configuration.....	32
4.2.9 S5 RTC Wake Settings .....	34
4.2.10 CSM Configuration.....	35
4.3 Chipset .....	36
4.3.1 Graphics Configuration .....	38
4.3.2 PEG Port Configuration .....	40
4.3.3 Memory Configuration .....	41
4.3.4 LCD Control .....	42
4.3.5 PCI Express Configuration.....	43
4.3.6 USB Configuration .....	44
4.4 Security .....	45
4.5 Boot .....	46
4.6 Save & Exit.....	47
<b>Appendix</b>	<b>48</b>
Appendix A: I/O Port Address Map .....	49
Appendix B: BIOS Memory Mapping.....	50
Appendix C: Interrupt Request Lines (IRQ) .....	52
Appendix D: Watchdog Timer (WDT) Setting.....	53
Appendix E: DIO Sample Code .....	55

## Copyright Notice

All Rights Reserved.

The information in this document is subject to change without prior notice in order to improve the reliability, design and function. It does not represent a commitment on the part of the manufacturer.

Under no circumstances will the manufacturer be liable for any direct, indirect, special, incidental, or consequential damages arising from the use or inability to use the product or documentation, even if advised of the possibility of such damages.

This document contains proprietary information protected by copyright. All rights are reserved. No part of this manual may be reproduced by any mechanical, electronic, or other means in any form without prior written permission of the manufacturer.

## Declaration of Conformity

### CE

The CE symbol on your product indicates that it is in compliance with the directives of the Union European (EU). A Certificate of Compliance is available by contacting Technical Support.

This product has passed the CE test for environmental specifications when shielded cables are used for external wiring. We recommend the use of shielded cables. This kind of cable is available from ARBOR. Please contact your local supplier for ordering information.

This product has passed the CE test for environmental specifications. Test conditions for passing included the equipment being operated within an industrial enclosure. In order to protect the product from being damaged by ESD (Electrostatic Discharge) and EMI leakage, we strongly recommend the use of CE-compliant industrial enclosure products.

#### Warning

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

### FCC Class B

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) This device may not cause harmful interference, and
- (2) This device must accept any interference received, including interference that may cause undesired operation.

### NOTE:

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

### RoHS

ARBOR Technology Corp. certifies that all components in its products are in compliance and conform to the European Union's Restriction of Use of Hazardous Substances in Electrical and Electronic Equipment (RoHS) Directive 2002/95/EC.

The above mentioned directive was published on 2/13/2003. The main purpose of the directive is to prohibit the use of lead, mercury, cadmium, hexavalent chromium, polybrominated biphenyls (PBB), and polybrominated diphenyl ethers (PBDE) in electrical and electronic products. Member states of the EU are to enforce by 7/1/2006.

ARBOR Technology Corp. hereby states that the listed products do not contain unintentional additions of lead, mercury, hex chrome, PBB or PBDB that exceed a maximum concentration value of 0.1% by weight or for cadmium exceed 0.01% by weight, per homogenous material. Homogenous material is defined as a substance or mixture of substances with uniform composition (such as solders, resins, plating, etc.). Lead-free solder is used for all terminations (Sn(96-96.5%), Ag(3.0-3.5%) and Cu(0.5%)).

### SVHC / REACH

To minimize the environmental impact and take more responsibility to the earth we live, Arbor hereby confirms all products comply with the restriction of SVHC (Substances of Very High Concern) in (EC) 1907/2006 (REACH --Registration, Evaluation, Authorization, and Restriction of Chemicals) regulated by the European Union.

All substances listed in SVHC < 0.1 % by weight (1000 ppm)

## Warning

Single Board Computers and their components contain very delicate Integrated Circuits (IC). To protect the Single Board Computer and its components against damage from static electricity, you should always follow the following precautions when handling it :

1. Disconnect your Single Board Computer from the power source when you want to work on the inside.
2. Hold the board by the edges and try not to touch the IC chips, leads or circuitry.
3. Use a grounded wrist strap when handling computer components.
4. Place components on a grounded antistatic pad or on the bag that comes with the Single Board Computer, whenever components are separated from the system.

## Replacing the Lithium Battery

Incorrect replacement of the lithium battery may lead to a risk of explosion.

The lithium battery must be replaced with an identical battery or a battery type recommended by the manufacturer.

Do not throw lithium batteries into the trash-can. It must be disposed of in accordance with local regulations concerning special waste.

## Technical Support

If you have any technical difficulties, please consult the user's manual first at:

<http://www.arbor-technology.com/>

Please do not hesitate to call or e-mail our customer service when you still cannot find out the answer.

<http://www.arbor-technology.com/>

E-mail:[info@arbor.com.tw](mailto:info@arbor.com.tw)

## **Warranty**

This product is warranted to be in good working order for a period of two years from the date of purchase. Should this product fail to be in good working order at any time during this period, we will, at our option, replace or repair it at no additional charge except as set forth in the following terms. This warranty does not apply to products damaged by misuse, modifications, accident or disaster.

Vendor assumes no liability for any damages, lost profits, lost savings or any other incidental or consequential damage resulting from the use, misuse of, or inability to use this product. Vendor will not be liable for any claim made by any other related party.

Vendors disclaim all other warranties, either expressed or implied, including but not limited to implied warranties of merchantability and fitness for a particular purpose, with respect to the hardware, the accompanying product's manual(s) and written materials, and any accompanying hardware. This limited warranty gives you specific legal rights.

Return authorization must be obtained from the vendor before returned merchandise will be accepted. Authorization can be obtained by calling or faxing the vendor and requesting a Return Merchandise Authorization (RMA) number. Returned goods should always be accompanied by a clear problem description.





# Chapter 1

# Introduction

### 1.1 The Product

The EmETXe-i89M0 is a space-conscious CPU board of 95 mm x 95 mm to take up only small footprint in your system. By the architecture of Type 6, the board has two high-performance connectors to promise stable data passing rate. The soldered onboard 6<sup>th</sup> Generation Intel® Core™ processor, along with integrated Intel® Graphics chipset, bring LVDS, and DDI solution for most monitors or LCD video panels.

For system configuration, the board is supported by AMI UEFI BIOS. EmETXe-i89M0 is an ideal choice for some demanding industrial control and data communications by its significant processing performance, low power consumption and these features:

- Soldered onboard 6<sup>th</sup> Generation Intel® Core™ i7-6822EQ
- Intel I219LM PCIe GbE PHY w/ iAMT
- Dual Channels 24-bit LVDS and 3 x DDI ports
- Support 3 independent displays
- 5V~20V Wide Range Voltage Input
- Wide Range Operating Temp.: -40 ~ 85°C

### 1.2 About This Manual

This user's manual provides general information and installation instructions about the product. This user's manual is intended for experienced users and integrators with hardware knowledge of personal computers. If you are not sure about any description in this booklet. Please consult your vendor before further handling.

## 1.3 Specifications

### Specifications

System	
CPU	Soldered onboard 6 <sup>th</sup> Generation Intel® Core™ i7-6822EQ 2.0GHz (Base)/ 2.8GHz (Turbo)
Memory	2 x DDR4-RS SO-DIMM sockets
BIOS	AMI UEFI BIOS
Watchdog Timer	1~255 levels reset
I/O	
USB 2.0	8 x USB 2.0 ports
USB 3.0	4 x USB SuperSpeed ports
Serial Port	2 x UART ports (RX/TX only)
Storage	4 x Serial ATA ports with 600MB/s HDD transfer rate
Expansion Bus	8 x PCIe x1 lanes, I2C Interface,GPIO
Ethernet Chipset	1 x Intel® i219LM PCIe GbE PHY with iAMT
Audio	HD audio link
TPM Function	TPM supported (OEM request)
Display	
Graphics Chipset	Intergrated Intel® HD Graphics 530
Graphics Interface	LCD: Dual Channels 24-bit LVDS
	3 x DDI ports
Mechanical & Environmental	
Power Requirement	5V~20V +/- 5% wide range voltage input, +5VSB
Power Consumption	2.58A@12V (i7-6822EQ typical)
Operating Temp.	-40 ~ 85°C (-40 ~ 185°F)
Operating Humidity	10 ~ 95% @ 85°C (non-condensing)
Dimension (L x W)	125 x 95 mm (4.9" x 3.7")

## 1.4 Inside the Package

Before you begin installing your single board, please make sure that the following materials have been shipped:



1 x EmETXe-i89M0 COM Express CPU Module



1 x Driver CD  
1 x Quick Installation Guide

If any of the above items is damaged or missing, contact your vendor immediately.

## 1.5 Ordering Information

EmETXe-i89M0-6822EQ	6 <sup>th</sup> Generation Intel® Core™ i7-6822EQ 2.0GHz (Base) / 2.8GHz (Turbo) COM Express® Basic Type 6 WT CPU module (MOQ required)
---------------------	---

### 1.5.1 Optional Accessories

HS-89M0-F1	Heat spreader with threaded standoffs (bore hole) (125x95x18mm)
HS-89M0-C1	Heat sink with FAN 125x95x39.7mm
PBE-1705-F1	COM Express® Type 6 evaluation carrier board with SIO F71869ED module in ATX form factor
CBK-03-1705-00	Cable kit <ul style="list-style-type: none"><li>• 1 x SATA cable</li><li>• 2 x COM Flat cables</li></ul>

## 1.6 The Installation Paths of CD Driver

The CPU module supports Windows 8.1 and 10. Find the necessary drivers on the CD that comes with your purchase. For different OS, the driver installation may vary slightly, but generally they are similar. **DO** install **Chipset**→**Graphic**→**Audio** before the rest to prevent errors.

Find the drivers on CD by the following paths:

### Windows 8.1&10

Driver	Path
Chipset	\\i89X\Chipset\Chipset_10.1.1.13_Public
Graphic	\\i89X\Graphic\IntelR Graphics Driver Production Version 15.40.16.64.4364
Audio	\\i89X\Audio\7687_PG436_Win10_Win8.1_WHQLx64
Ethernet	\\i89X\Ethernet
USB3.0	\\i89X\USB3.0\win8.1 64bit\Intel_USB_3.0_xHC_Adaptation_Driver_MR1_Release_1.0.1.45_PV <b>(For Win 8.1 only)</b>
ME	\\i89X\ME\Intel(R)_ME_11.0_Corporate_11.0.0.1202
RAID	\\i89X\RAID\Intel Rapid Storage Technology Driver 14.8.0.1042

---

This page is intentionally left blank.



# Chapter 2

## Board Overview

## 2.1 What Is “COM Express®”?

With more and more demands on small and embedded industrial boards, a multi-functional COM (Computer-on-Module) surfaces as a great solution.

COM Express® supports seven pin-out types applying to Basic and Extended form factors:

Module Type 1 and 10 support single connector with two rows (220 pins).

Module Type 2, 3, 4, 5 and 6 support two connectors with four rows (440 pins).

EmETXe-i89M0 is a Type-6 module.

Difference between Standard Type 6 and EmETXe-i89M0 is listed as below:

Module Type	Standard Type 6	EmETXe-i89M0
Connectors	2	2
Connector Rows	A, B, C, D	A, B, C, D
PCIe Lanes (Max)	24	24
LAN (Max)	1	1
Serial Ports (Max)	2	2
Digital Display I/F (Max)	3	3
USB 3.0 Ports (Max)	4	4

Row AB provides pins for PCI Express, SATA, LVDS, LCD channel, LPC bus, system and power management, VGA, LAN, and power and ground interfaces.

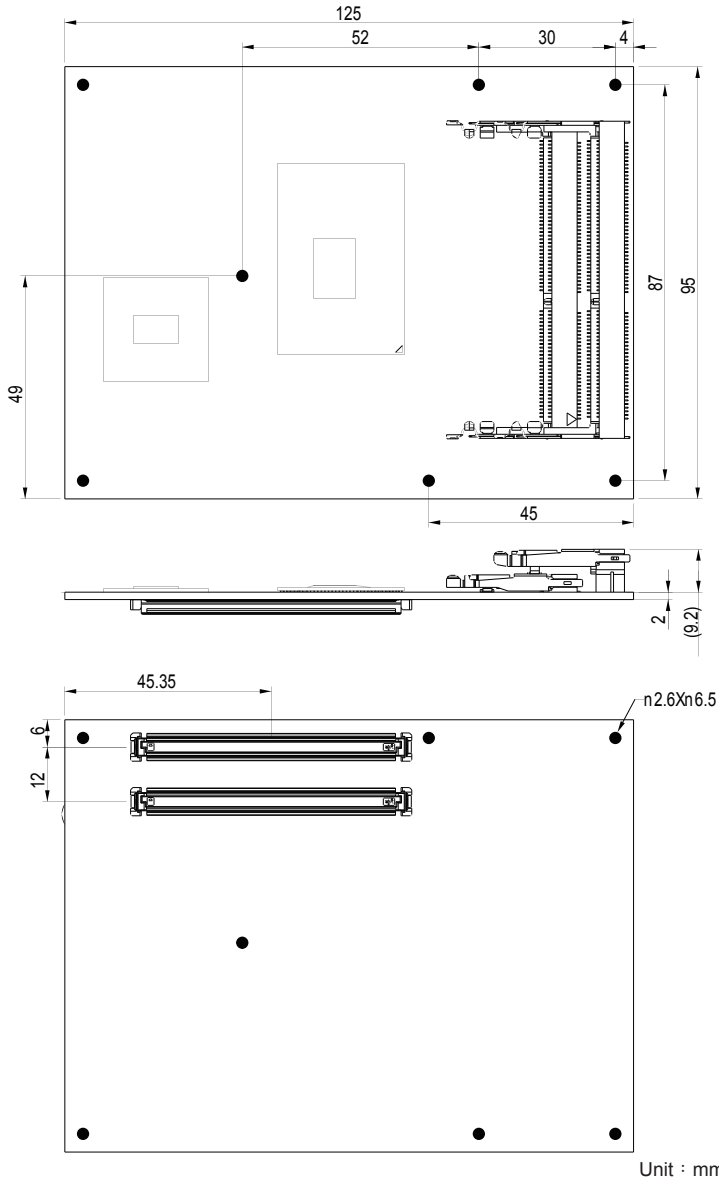
Row CD provides SDVO and legacy PCI signals next to additional PCI Express, LAN and power and ground signals. The COM are targeted at following applications:

- Retail & Advertising
- Medical
- Test & Measurement
- Gaming & Entertainment
- Industrial & Automation
- Military & Government
- Security

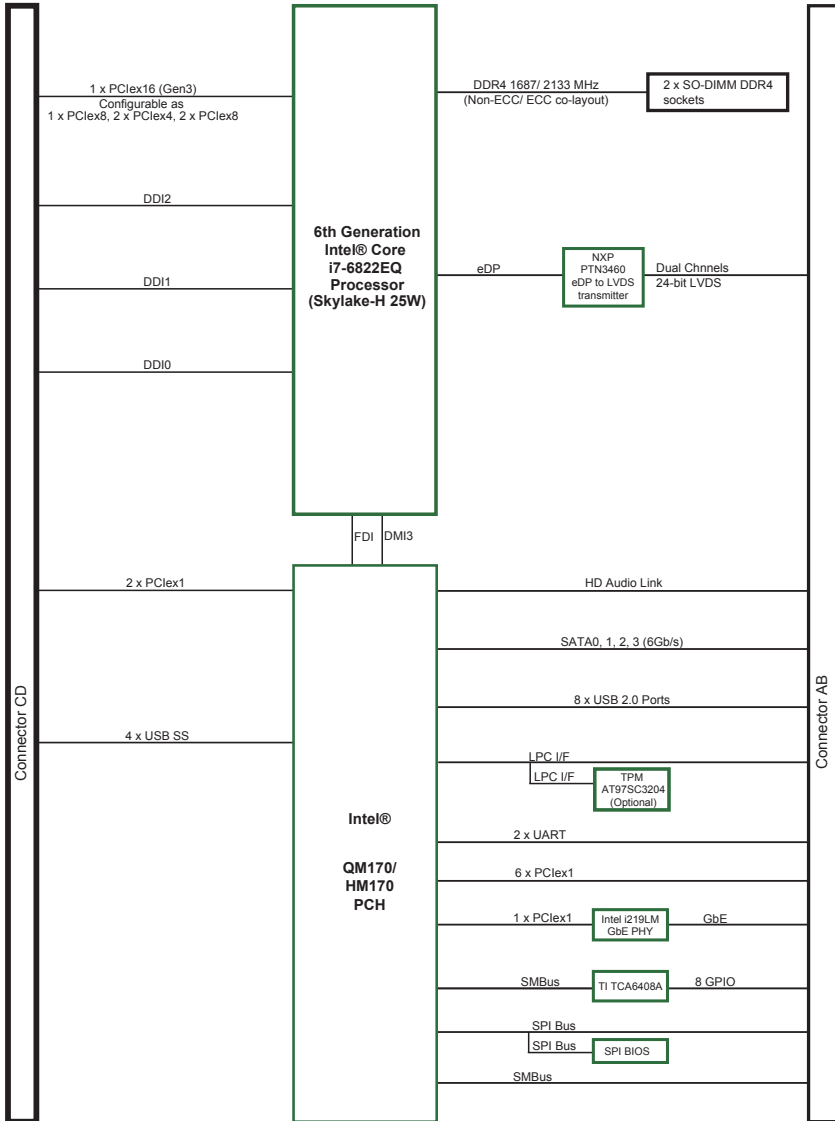


## 2.2 Board Dimensions

The following illustration shows the dimension of EmETXe-i89M0, with the measurements in width, depth, and height called out.



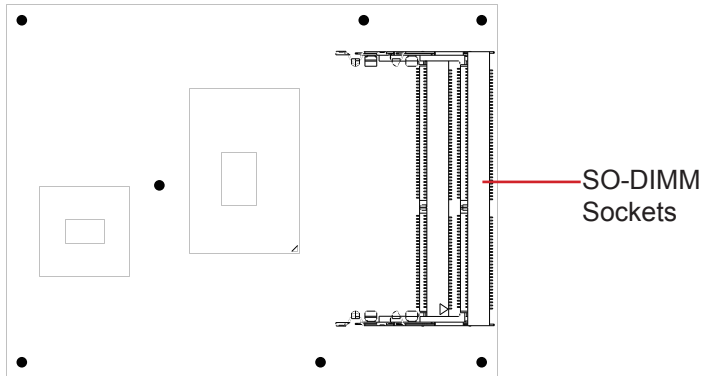
## 2.3 Block Diagram



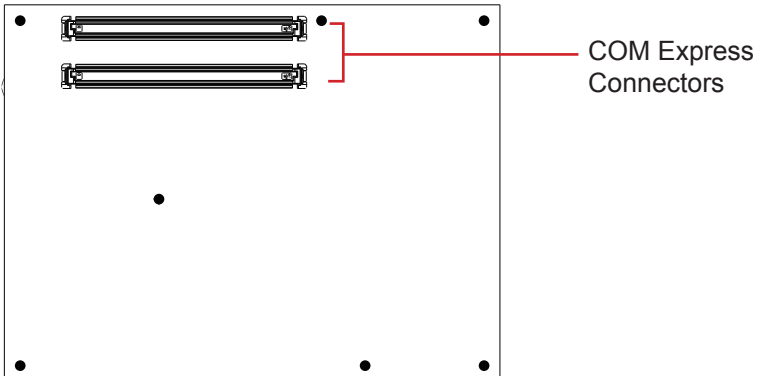
## 2.4 Connector Pin Definition

Being a most commonly-used Type 6, the EmETXe-i89M0 features two board-to-board connectors on bottom side.

### Top Side



### Bottom Side



## COM Express AB Connector (bottom side)

B1	GND (FIXED)	GND (FIXED)	A1	B56	PCIE_RX4-	PCIE_TX4-	A56
B2	GBE0_ACT#	GBE0_MDI3-	A2	B57	GPO2	GND	A57
B3	LPC_FRAME#	GBE0_MDI3+	A3	B58	PCIE_RX3+	PCIE_TX3+	A58
B4	LPC_AD0	GBE0_LINK100#	A4	B59	PCIE_RX3-	PCIE_TX3-	A59
B5	LPC_AD1	GBE0_LINK1000#	A5	B60	GND	GND	A60
B6	LPC_AD2	GBE0_MDI2-	A6	B61	PCIE_RX2+	PCIE_TX2+	A61
B7	LPC_AD3	GBE0_MDI2+	A7	B62	PCIE_RX2-	PCIE_TX2-	A62
B8	LPC_DRQ0#	GBE0_LINK#	A8	B63	GPO3	GP11	A63
B9	LPC_DRQ1#	GBE0_MDI1-	A9	B64	PCIE_RX1+	PCIE_TX1+	A64
B10	LPC_CLK	GBE0_MDI1+	A10	B65	PCIE_RX1-	PCIE_TX1-	A65
B11	GND (FIXED)	GND (FIXED)	A11	B66	WAKE0#	GND	A66
B12	PWRBTN#	GBE0_MDI0-	A12	B67	WAKE1#	GP12	A67
B13	SMB_CK	GBE0_MDI0+	A13	B68	PCIE_RX0+	PCIE_TX0+	A68
B14	SMB_DAT	GBE0_CTRF#	A14	B69	PCIE_RX0-	PCIE_TX0-	A69
B15	SMB_ALERT#	SUS_S3#	A15	B70	GND	GND	A70
B16	SATA1_TX+	SATA0_TX+	A16	B71	LVDS_B0+	LVDS_A0+	A71
B17	SATA1_TX-	SATA0_TX-	A17	B72	LVDS_B0-	LVDS_A0-	A72
B18	SUS_STAT#	SUS_S#	A18	B73	LVDS_B1+	LVDS_A1+	A73
B19	SATA1_RX+	SATA0_RX+	A19	B74	LVDS_B1-	LVDS_A1-	A74
B20	SATA1_RX-	SATA0_RX-	A20	B75	LVDS_B2+	LVDS_A2+	A75
B21	GND (FIXED)	GND (FIXED)	A21	B76	LVDS_B2-	LVDS_A2-	A76
B22	SATA3_TX+	SATA2_TX+	A22	B77	LVDS_B3+	LVDS_VDD_EN	A77
B23	SATA3_TX-	SATA2_TX-	A23	B78	LVDS_B3-	LVDS_A3+	A78
B24	PWR_OK	SUS_S5#	A24	B79	LVDS_BKLT_EN	LVDS_A3-	A79
B25	SATA3_RX+	SATA2_RX+	A25	B80	GND	GND	A80
B26	SATA3_RX-	SATA2_RX-	A26	B81	LVDS_B_CK+	LVDS_A_CK+	A81
B27	WDT	BATLOW#	A27	B82	LVDS_B_CK-	LVDS_A_CK-	A82
B28	N/C	SATA_ACT#	A28	B83	LVDS_BKLT_CTRL	LVDS_I2C_CK	A83
B29	AC_SDIN1	AC_SYNC	A29	B84	VCC_5V_SBY	LVDS_I2C_DAT	A84
B30	AC_SDIN0	AC_RST#	A30	B85	VCC_5V_SBY	GP13	A85
B31	GND	GND	A31	B86	VCC_5V_SBY	RSVD	A86
B32	SPKR	AC_BITCLK	A32	B87	VCC_5V_SBY	RSVD	A87
B33	I2C_CK	AC_SDOUT	A33	B88	BIOS_DISABLE1#	PCIE_CLK+	A88
B34	I2C_DAT	BIOS_DISABLE0#	A34	B89	N/C	PCIE_CLK-	A89
B35	THR#	THR#	A35	B90	GND	GND	A90
B36	USB7-	USB6-	A36	B91	N/C	SPL_POWER	A91
B37	USB7+	USB6+	A37	B92	N/C	SPL_MISO	A92
B38	USB_4_5_OC#	USB_6_7_OC#	A38	B93	N/C	GPO0	A93
B39	USB5-	USB4-	A39	B94	N/C	SPL_CLK	A94
B40	USB5+	USB4+	A40	B95	N/C	SPL_MOSI	A95
B41	GND	GND	A41	B96	N/C	TPM_PP	A96
B42	USB3-	USB2-	A42	B97	SPL_CS#	N/C	A97
B43	USB3+	USB2+	A43	B98	N/C	UART_TX0	A98
B44	USB_0_1_OC#	USB_2_3_OC#	A44	B99	N/C	UART_RX0	A99
B45	USB1-	USB0-	A45	B100	GND	GND	A100
B46	USB1+	USB0+	A46	B101	FAN_PWMOUT	UART_TX1	A101
B47	EXCD1_PERST#	VCC_RTC	A47	B102	FAN_TACHIN	UART_RX1	A102
B48	EXCD1_CPPE#	EXCD0_PERST#	A48	B103	SLEEP#	LID#	A103
B49	SYS_RESET#	EXCD0_CPPE#	A49	B104	VCC_12V	VCC_12V	A104
B50	CB_RESET#	LPC_SERIRQ	A50	B105	VCC_12V	VCC_12V	A105
B51	GND	GND	A51	B106	VCC_12V	VCC_12V	A106
B52	PCIE_RX5+	PCIE_TX5+	A52	B107	VCC_12V	VCC_12V	A107
B53	PCIE_RX5-	PCIE_TX5-	A53	B108	VCC_12V	VCC_12V	A108
B54	GPO1	GP10	A54	B109	VCC_12V	VCC_12V	A109
B55	PCIE_RX4+	PCIE_TX4+	A55	B110	GND	GND	A110

## COM Express CD Connector (bottom side)

D1	GND (FIXED)	GND (FIXED)	C1	D56	PEG_TX1-	PEG_RX1-	C56
D2	GND	GND	C2	D57	TYPE2#	NC	C57
D3	USB_SSTX0-	USB_SSRX0-	C3	D58	PEG_TX2+	PEG_RX2+	C58
D4	USB_SSTX0+	USB_SSRX0+	C4	D59	PEG_TX2-	PEG_RX2-	C59
D5	GND	GND	C5	D60	GND (FIXED)	GND (FIXED)	C60
D6	USB_SSTX1-	USB_SSRX1-	C6	D61	PEG_TX3+	PEG_RX3+	C61
D7	USB_SSTX1+	USB_SSRX1+	C7	D62	PEG_TX3-	PEG_RX3-	C62
D8	GND	GND	C8	D63	RSVD	RSVD	C63
D9	USB_SSTX2-	USB_SSRX2-	C9	D64	RSVD	RSVD	C64
D10	USB_SSTX2+	USB_SSRX2+	C10	D65	PEG_TX4+	PEG_RX4+	C65
D11	GND (FIXED)	GND (FIXED)	C11	D66	PEG_TX4-	PEG_RX4-	C66
D12	USB_SSTX3-	USB_SSRX3-	C12	D67	GND	RSVD	C67
D13	USB_SSTX3+	USB_SSRX3+	C13	D68	PEG_TX5+	PEG_RX5+	C68
D14	GND	GND	C14	D69	PEG_TX5-	PEG_RX5-	C69
D15	DDI1_CTRLCLK_AUX+	NC	C15	D70	GND (FIXED)	GND (FIXED)	C70
D16	DDI1_CTRLCLK_AUX-	NC	C16	D71	PEG_TX6+	PEG_RX6+	C71
D17	RSVD	RSVD	C17	D72	PEG_TX6-	PEG_RX6-	C72
D18	RSVD	RSVD	C18	D73	GND	GND	C73
D19	PCIE_TX6+	PCIE_RX6+	C19	D74	PEG_TX7+	PEG_RX7+	C74
D20	PCIE_TX6-	PCIE_RX6-	C20	D75	PEG_TX7-	PEG_RX7-	C75
D21	GND(FIXED)	GND(FIXED)	C21	D76	GND	GND	C76
D22	PCIE_TX7+	PCIE_RX7+	C22	D77	RSVD	RSVD	C77
D23	PCIE_TX7-	PCIE_RX7-	C23	D78	PEG_TX8+	PEG_RX8+	C78
D24	RSVD	DDI1_HPD	C24	D79	PEG_TX8-	PEG_RX8-	C79
D25	RSVD	NC	C25	D80	GND (FIXED)	GND (FIXED)	C80
D26	DDI1_PAIR0+	NC	C26	D81	PEG_TX9+	PEG_RX9+	C81
D27	DDI1_PAIR0-	RSVD	C27	D82	PEG_TX9-	PEG_RX9-	C82
D28	RSVD	RSVD	C28	D83	RSVD	RSVD	C83
D29	DDI1_PAIR1+	NC	C29	D84	GND	GND	C84
D30	DDI1_PAIR1-	NC	C30	D85	PEG_TX10+	PEG_RX10+	C85
D31	GND(FIXED)	GND (FIXED)	C31	D86	PEG_TX10-	PEG_RX10-	C86
D32	DDI1_PAIR2+	DDI2_CTRLCLK_AUX+	C32	D87	GND	GND	C87
D33	DDI1_PAIR2-	DDI2_CTRLDATA_AUX-	C33	D88	PEG_TX11+	PEG_RX11+	C88
D34	DDI1_DDC_AUX_SEL	DDI2_DDC_AUX_SEL	C34	D89	PEG_TX11-	PEG_RX11-	C89
D35	RSVD	RSVD	C35	D90	GND (FIXED)	GND (FIXED)	C90
D36	DDI1_PAIR3+	DDI3_CTRLCLK_AUX+	C36	D91	PEG_TX12+	PEG_RX12+	C91
D37	DDI1_PAIR3-	DDI3_CTRLDATA_AUX-	C37	D92	PEG_TX12-	PEG_RX12-	C92
D38	RSCD	DDI3_DDC_AUX_SEL	C38	D93	GND	GND	C93
D39	DDI2_PAIR0+	DDI3_PAIR0+	C39	D94	PEG_TX13+	PEG_RX13+	C94
D40	DDI2_PAIR0-	DDI3_PAIR0-	C40	D95	PEG_TX13-	PEG_RX13-	C95
D41	GND(FIXED)	GND(FIXED)	C41	D96	GND	GND	C96
D42	DDI2_PAIR1+	DDI3_PAIR1+	C42	D97	RSVD	RSVD	C97
D43	DDI2_PAIR1-	DDI3_PAIR1-	C43	D98	PEG_TX14+	PEG_RX14+	C98
D44	DDI2_HPD	DDI3_HPD	C44	D99	PEG_TX14-	PEG_RX14-	C99
D45	RSVD	RSVD	C45	D100	GND (FIXED)	GND (FIXED)	C100
D46	DDI2_PAIR2+	DDI3_PAIR2+	C46	D101	PEG_TX15+	PEG_RX15+	C101
D47	DDI2_PAIR2-	DDI3_PAIR2-	C47	D102	PEG_TX15-	PEG_RX15-	C102
D48	RSVD	RSVD	C48	D103	GND	GND	C103
D49	DDI2_PAIR3+	DDI3_PAIR3+	C49	D104	VCC_12V	VCC_12V	C104
D50	DDI2_PAIR3-	DDI3_PAIR3-	C50	D105	VCC_12V	VCC_12V	C105
D51	GND (FIXED)	GND (FIXED)	C51	D106	VCC_12V	VCC_12V	C106
D52	PEG_TX0+	PEG_RX0+	C52	D107	VCC_12V	VCC_12V	C107
D53	PEG_TX0-	PEG_RX0-	C53	D108	VCC_12V	VCC_12V	C108
D54	PEG_LANE_RV#	NC	C54	D109	VCC_12V	VCC_12V	C109
D55	PEG_TX1+	PEG_RX1+	C55	D110	GND (FIXED)	GND (FIXED)	C110

---

This page is intentionally left blank.

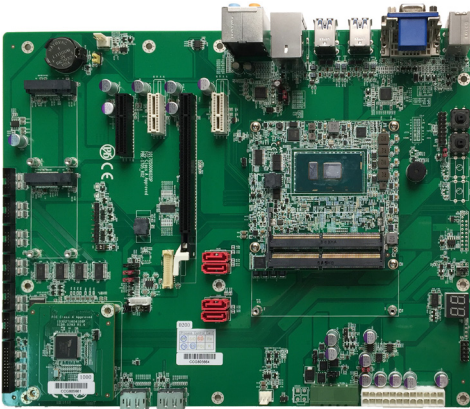
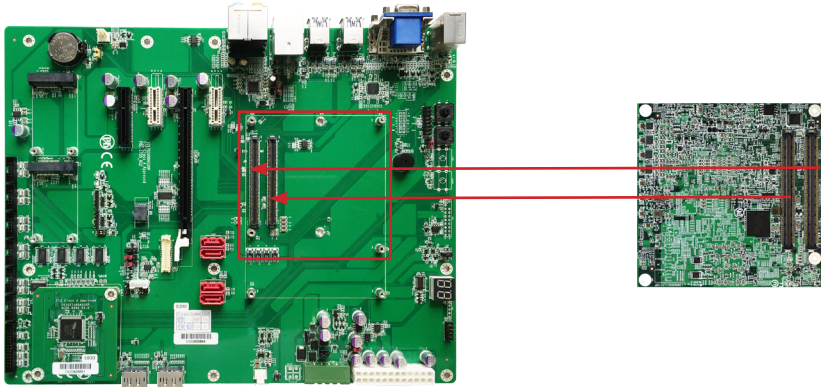


# Chapter 3

## Installation & Maintenance

### 3.1 Installing the CPU Module on Carrier Board

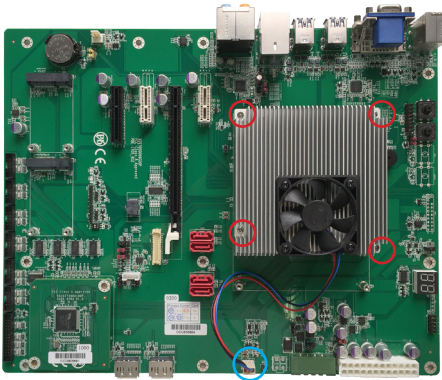
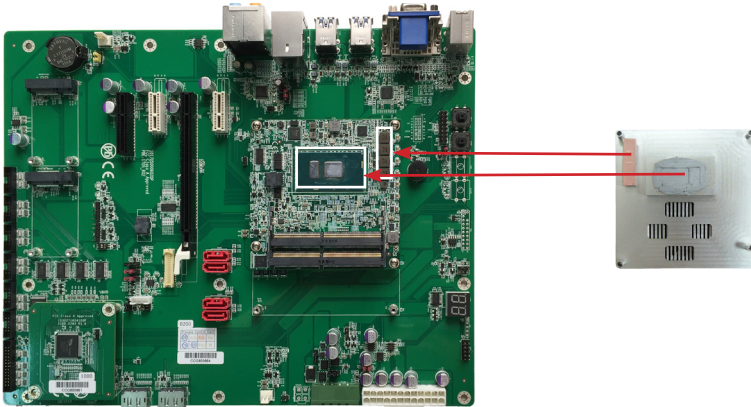
1. Find the COM Express connectors on carrier board PBE-1705, which is available in Section [1.5.1 Optional Accessories on page 4](#).
2. Embed EmETXe-i89M0 into PBE-1705 via COM Express connectors as below; that is, COM Express AB to AB and CD to CD.





### 3.2 Installing the Heatsink

1. Locate EmETXe-i89M0 mounted on PBE-1705.
2. Prepare the heatspred included in optional accessories. (See section [1.5.1 Optional Accessories on page 4](#)) Put heatspred on the CPU module and lock it. Make sure thermal grease in contact with CPU and chipset on CPU module. Plug power cable into appropriate connector if there is a fan.



---

This page is intentionally left blank.

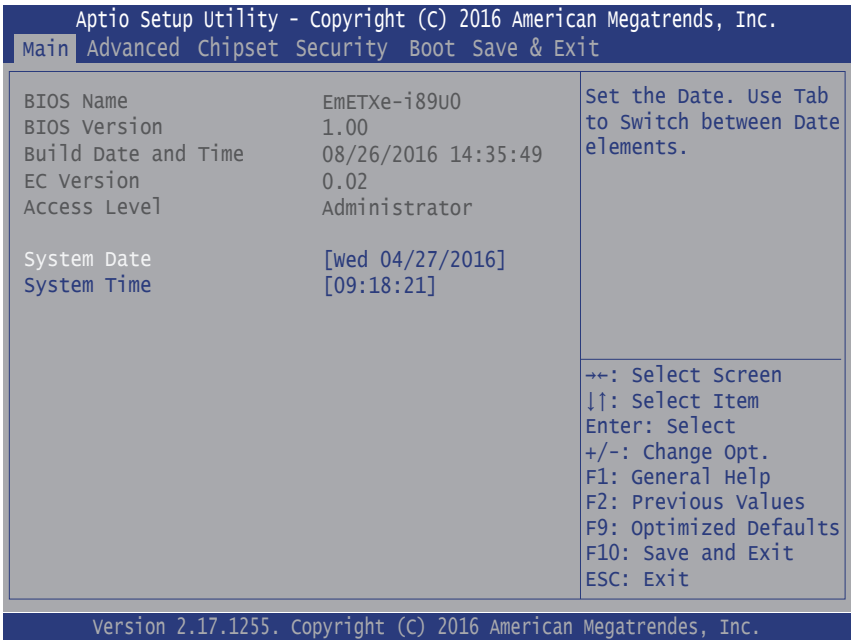
# Chapter 4

# BIOS

### 4.1 Main

The AMI BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS RAM of the system stores the Setup utility and configurations. When you turn on the computer, the AMI BIOS is immediately activated. To enter the BIOS SETUP UTILITY, press “Delete” once the power is turned on.

The **Main Setup** screen lists the following information:



Setting	Description
System Date	<p>Set the system date. Use Tab to switch between Data elements. Note that the ‘Day’ automatically changes when you set the date.</p> <ul style="list-style-type: none"> <li>▶ The date format is:                             <ul style="list-style-type: none"> <li><b>Day:</b> Sun to Sat</li> <li><b>Month:</b> 1 to 12</li> <li><b>Date:</b> 1 to 31</li> <li><b>Year:</b> 1998 to 2099</li> </ul> </li> </ul>

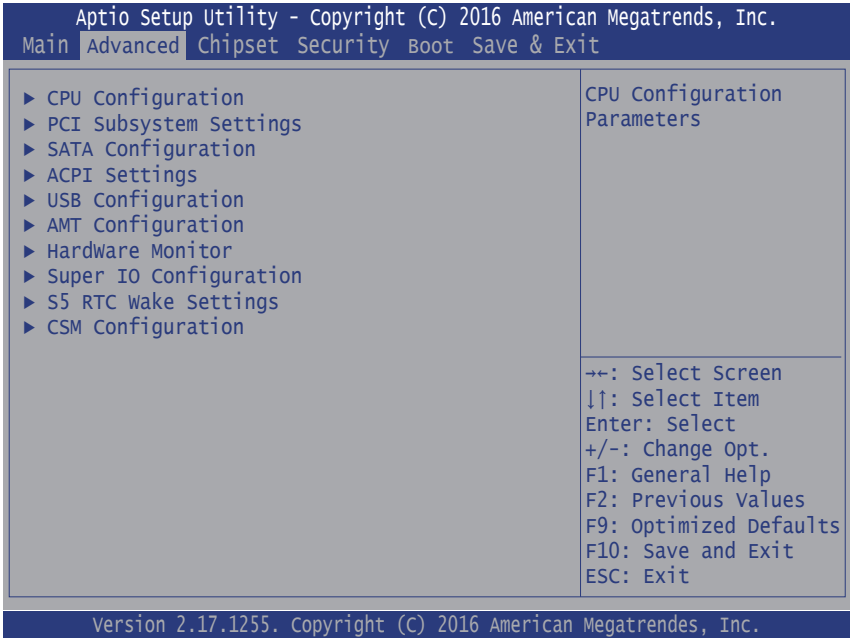
System Time	<p>Set the system time. Use Tab to switch between Time elements.</p> <p>▶ The time format is: <b>Hour:</b> 00 to 23 <b>Minute:</b> 00 to 59 <b>Second:</b> 00 to 59</p>
-------------	---

## Key Commands

BIOS Setup Utility is mainly a key-based navigation interface. Please refer to the following key command instructions for navigation process.

Keystroke	Function
◀ ▶	Move to highlight a particular configuration screen from the top menu bar / Move to highlight items on the screen
▼ ▲	Move to highlight previous/next item
Enter	Select and access a setup item/field
Esc	On the Main Menu – Quit the setup and not save changes into CMOS (a message screen will display and ask you to select “OK” or “Cancel” for exiting and discarding changes. Use “←” and “→” to select and press “Enter” to confirm) On the Sub Menu – Exit current page and return to main menu
Page Up / +	Increase the numeric value on a selected setup item / make change
Page Down / -	Decrease the numeric value on a selected setup item / make change
F1	Activate “General Help” screen
F10	Save the changes that have been made in the setup and exit. (a message screen will display and ask you to select “OK” or “Cancel” for exiting and saving changes. Use “←” and “→” to select and press “Enter” to confirm)

## 4.2 Advanced



Setting	Description
CPU Configuration	See section <a href="#">4.2.1 CPU Configuration on page 23</a>
PCI Subsystem Settings	See section <a href="#">4.2.2 PCI Subsystem Settings on page 25</a>
SATA Configuration	See section <a href="#">4.2.3 SATA Configuration on page 26</a>
ACPI Settings	See section <a href="#">4.2.4 ACPI Settings on page 27</a>
USB Configuration	See section <a href="#">4.2.5 USB Configuration on page 28</a>
AMT Configuration	See section <a href="#">4.2.6 AMT Configuration on page 30</a>
Hardware Monitor	See section <a href="#">4.2.7 Hardware Monitor on page 31</a>
Super IO Configuration	See section <a href="#">4.2.8 Super IO Configuration on page 32</a>
S5 RTC Wake Settings	See section <a href="#">4.2.9 S5 RTC Wake Settings on page 34</a>
CSM Configuration	See section <a href="#">4.2.10 CSM Configuration on page 35</a>

## 4.2.1 CPU Configuration

Aptio Setup Utility - Copyright (C) 2016 American Megatrends, Inc.

Advanced

CPU Configuration		Enabled for windows XP and Linux (OS optimized for Hyper-Threading Technology) and Disabled for other OS (OS not optimized for Hyper-Threading Technology). When disabled only one thread per enabled core is enabled.
Intel(R) Core(TM) i7-6822EQ CPU @ 2.00GHz		
CPU Signature	506E3	
Microcode Patch	7C	
Max CPU Speed	2000 MHz	
Min CPU Speed	800 MHz	
CPU Speed	2000 MHz	
Processor Cores	4	
L1 Data Cache	32 KB x 4	
L1 Code Cache	32 KB x 4	
L2 Cache	256 KB x 2	
L3 Cache	8 MB	
L4 Cache	Not Present	
Hyper-threading	[Enabled]	
Active Processor Cores	[All]	
Intel Virtualization Technology	[Enabled]	
Boot performance Mode	[Max Non-Turbo Performance]	
Intel (R) SpeedStep (tm)	[Enabled]	
Turbo Mode	[Enabled]	
CPU C states	[Disabled]	

++: Select Screen  
 ↓↑: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F9: Optimized Defaults  
 F10: Save and Exit  
 ESC: Exit

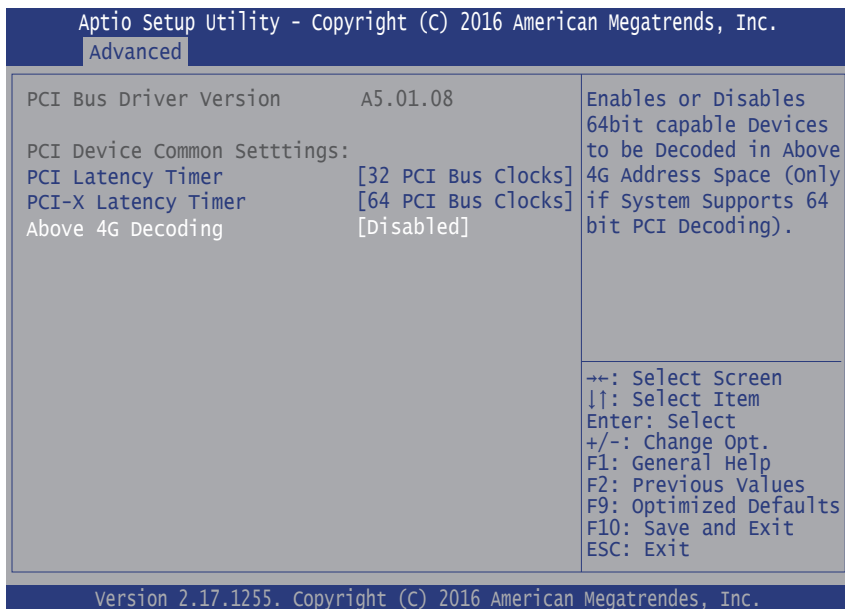
Version 2.17.1255. Copyright (C) 2016 American Megatrends, Inc.

Setting	Description
Hyper-threading	<b>Enabled</b> (default) for Windows XP and Linux (OS optimized for Hyper-Threading Technology) and <b>Disabled</b> for other OS (OS not optimized or Hyper-Threading Technology). When disabled only one thread per enabled core is enabled.
Active Processor Cores	Number of cores to enable in each processor package. ▶ Options: <b>All</b> (default), <b>1</b> , <b>2</b> and <b>3</b> .
Intel Virtualization Technology	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology ▶ Options: <b>Enabled</b> (default) or <b>Disabled</b>

Boot performance Mode	Set the performance state that the BIOS will set before the OS handoff. ▶ Options: <b>Max Battery</b> , <b>Max Non-Turbo Performance</b> (default) and <b>Turbo Performance</b> .
Intel (R) Speed Step (tm)	<b>Enable</b> (default)/ <b>Disable</b> Intel SpeedStep
Turbo Mode	Only available when Intel Speed Step is <b>Enabled</b> . <b>Enable</b> (default)/ <b>Disable</b> Turbo Mode
CPU C States	<b>Enable /Disable</b> (default) CPU C States

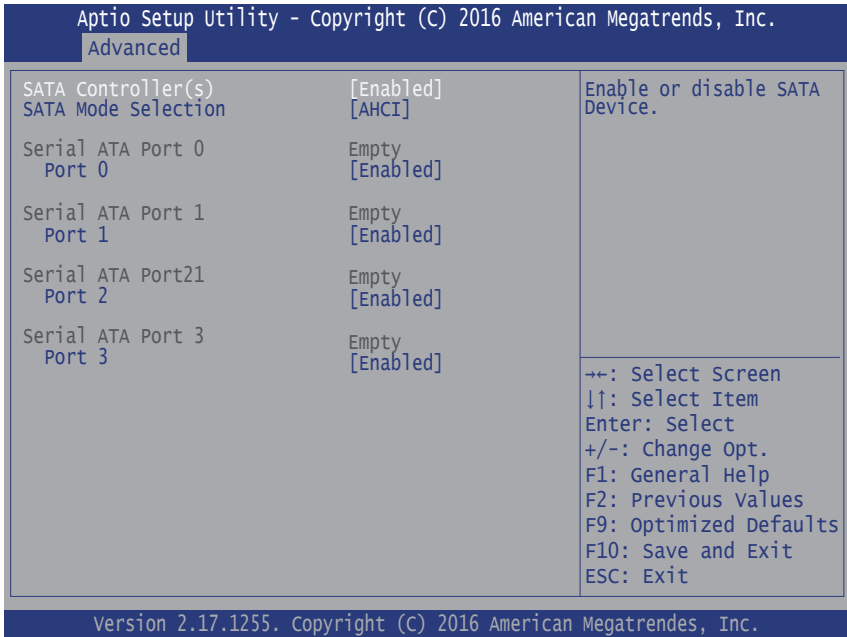


## 4.2.2 PCI Subsystem Settings



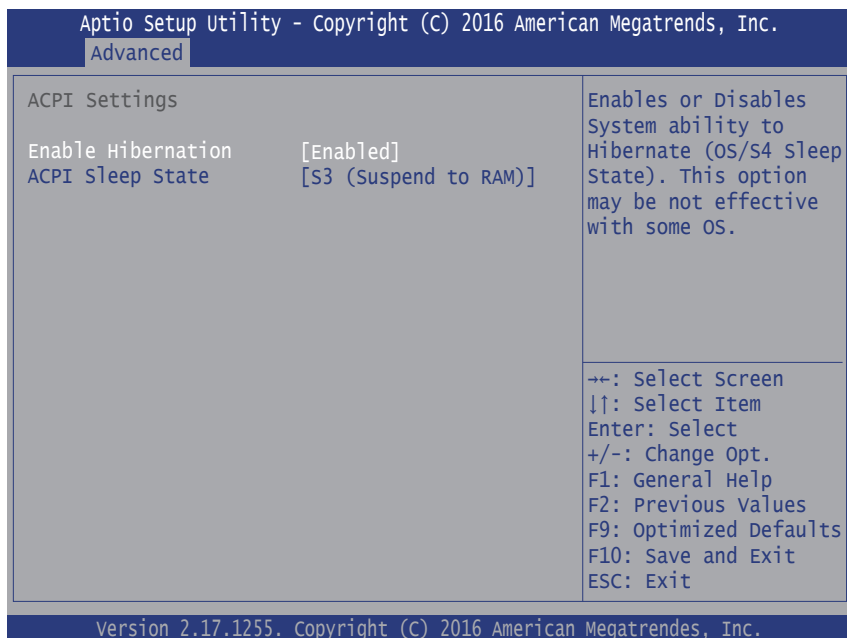
Setting	Description
PCI Latency Timer	Value to be programmed into PCI Latency Timer Register. ► <b>32</b> (default), <b>64</b> , <b>96</b> , <b>128</b> , <b>160</b> , <b>192</b> , <b>224</b> and <b>248 PCI Bus Clocks</b> .
PCI-X Latency Timer	Value to be programmed into PCI-X Latency Timer Register. ► <b>32</b> , <b>64</b> (default), <b>96</b> , <b>128</b> , <b>160</b> , <b>192</b> , <b>224</b> and <b>248 PCI Bus Clocks</b> .
Above 4G Decoding	<b>Enable/Disable</b> (default) 64bit capable Devices to be Decoded in Above 4G Address Space (Only if System Supports 64 bit PCI Decoding).

### 4.2.3 SATA Configuration



Setting	Description
SATA Controller(s)	<b>Enable</b> (default) or <b>disable</b> SATA Device.
SATA Mode Selection	Determines how SATA controller(s) operate. ► Options: <b>AHCI</b> (default) and <b>RAID</b>
Port 0/1/2/3	<b>Enable</b> (default) or <b>disable</b> SATA Port.

## 4.2.4 ACPI Settings



Setting	Description
Enable Hibernation	<b>Enable</b> (default) or <b>Disable</b> System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed. <ul style="list-style-type: none"> <li>▶ Options: <b>Suspend Disabled</b> and <b>S3 (Suspend to RAM)</b> (default).</li> </ul>

### 4.2.5 USB Configuration

Aptio Setup Utility - Copyright (C) 2016 American Megatrends, Inc.

Advanced

USB Configuration		Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications.
USB Module Version	14	
USB Devices:	1 XHCI	
USB Devices:	1 Keyboard	
Legacy USB Support	[Enabled]	
XHCI Hand-off	[Enabled]	
USB Mass Storage Driver Support	[Enabled]	
Port 60/64 Emulation	[Disabled]	
USB hardware delays and time-outs:		
USB Transfer time-out	[20 sec]	
Device reset time-out	[20 sec]	
Device power-up delay	[Auto]	

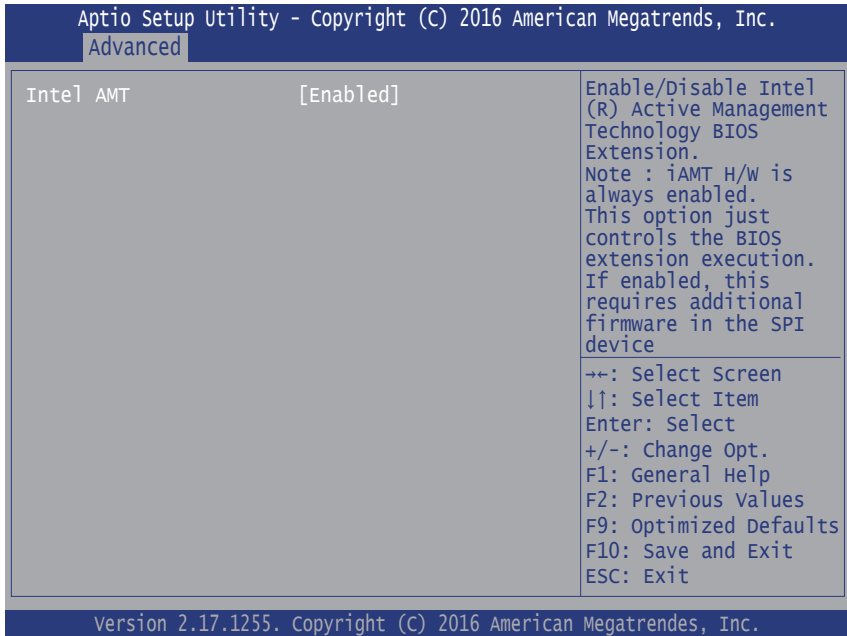
→←: Select Screen  
 ↓↑: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F9: Optimized Defaults  
 F10: Save and Exit  
 ESC: Exit

Version 2.17.1255. Copyright (C) 2016 American Megatrends, Inc.

Setting	Description
Legacy USB Support	Sets legacy USB support. ► Options: <b>Enabled</b> (default), <b>Disabled</b> and <b>Auto</b> . <b>AUTO</b> option disables legacy support if no USB devices are connected. <b>Disable</b> option will keep USB devices available only for EFI applications.
XHCI Hand-off	<b>Enable</b> (default) or <b>Disable</b> XHCI Hand-off This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
USB Mass Storage Driver Support	<b>Enable</b> (default) or <b>Disable</b> USB Mass Storage Driver Support.

USB hardware delay and time-out	
USB Transfer time-out	<p>Use this item to set the time-out value for control, bulk, and interrupt transfers.</p> <ul style="list-style-type: none"><li>▶ Options available are: <b>1 sec, 5 sec, 10 sec, 20 sec</b> (default)</li></ul>
Device reset time-out	<p>Use this item to set USB mass storage device start unit command time-out.</p> <ul style="list-style-type: none"><li>▶ Options available are: <b>10 sec, 20 sec</b> (default), <b>30 sec, 40 sec</b></li></ul>
Device power-up delay	<p>Use this item to set maximum time the device will take before it properly reports itself to the host controller.</p> <ul style="list-style-type: none"><li>▶ Options available are: <b>Auto</b> (Default): 'Auto' uses default value: for a root port it is 100 ms, for a hub port the delay is taken from hub descriptor. <b>Manual</b>: Select <b>Manual</b> you can set value for the following sub-item: '<b>Device Power-up delay in seconds</b>', the delay range in from 1 to 40 seconds, in one second increments.</li></ul>

### 4.2.6 AMT Configuration



Setting	Description
Intel AMT	<b>Enable</b> (default) / <b>Disable</b> Intel(R)Active Management Technology BIOS Extension. Note : iAMT H/W is always enabled. This option just controls the BIOS extension execution. If enabled, this requires additional firmware in the SPI device.

## 4.2.7 Hardware Monitor

Aptio Setup Utility - Copyright (C) 2016 American Megatrends, Inc.

Advanced

PC Health Status

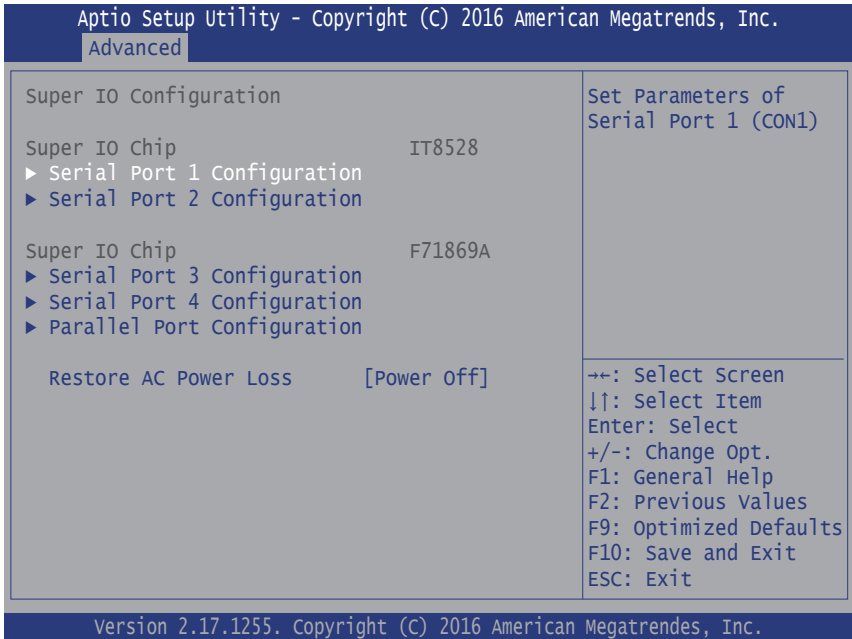
CPU Temperature	: +37°C
Fan1 Speed	: 4607 RPM
VCORE	: +0.858 V
VCCDU	: +1.189 V
VIN	: +11.942 V

→: Select Screen  
↓↑: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save and Exit  
ESC: Exit

Version 2.17.1255. Copyright (C) 2016 American Megatrends, Inc.

Access this submenu to monitor the hardware status.

## 4.2.8 Super IO Configuration



Setting	Description
Serial Port 1/2/3/4 & Parallel Port Configuration	See next page.
Restore AC Power Loss	Specify what state to go to when power is re-applied after a power failure. <ul style="list-style-type: none"> <li>Options: <b>Last State</b>, <b>Power On</b> and <b>Power Off</b> (default)</li> </ul>



## Serial Port 1/2/3/4 Configuration

Setting	Description
Serial Port	<b>Enable</b> (default) or <b>Disable</b> Serial Port (COM).
Change Settings	<p>Select an optimal setting for Super IO device.</p> <ul style="list-style-type: none"> <li>▶ Options for Serial Port 1:           <ul style="list-style-type: none"> <li><b>Auto</b>;</li> <li><b>IO=3F8h; IRQ=4</b> (default) ;</li> <li><b>IO=3F8h; IRQ=3, 4, 7, 12</b>;</li> <li><b>IO=2F8h; IRQ=3, 4, 7, 12</b>;</li> </ul> </li> <li>▶ Options for Serial Port 2:           <ul style="list-style-type: none"> <li><b>Auto</b></li> <li><b>IO=2F8h; IRQ=3</b> (default)</li> <li><b>IO=3F8h; IRQ=3, 4, 7, 12</b></li> <li><b>IO=2F8h; IRQ=3, 4, 7, 12</b></li> </ul> </li> <li>▶ Options for Serial Port 3:           <ul style="list-style-type: none"> <li><b>Auto</b></li> <li><b>IO=3E8h; IRQ=11</b> (default)</li> <li><b>IO=3E8h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=2E8h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=2F0h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=2E0h; IRQ=7, 10, 11, 12</b></li> </ul> </li> <li>▶ Options for Serial Port 4:           <ul style="list-style-type: none"> <li><b>Auto</b></li> <li><b>IO=2E8h; IRQ=10</b> (default)</li> <li><b>IO=3E8h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=2E8h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=2F0h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=2E0h; IRQ=7, 10, 11, 12</b></li> </ul> </li> </ul>

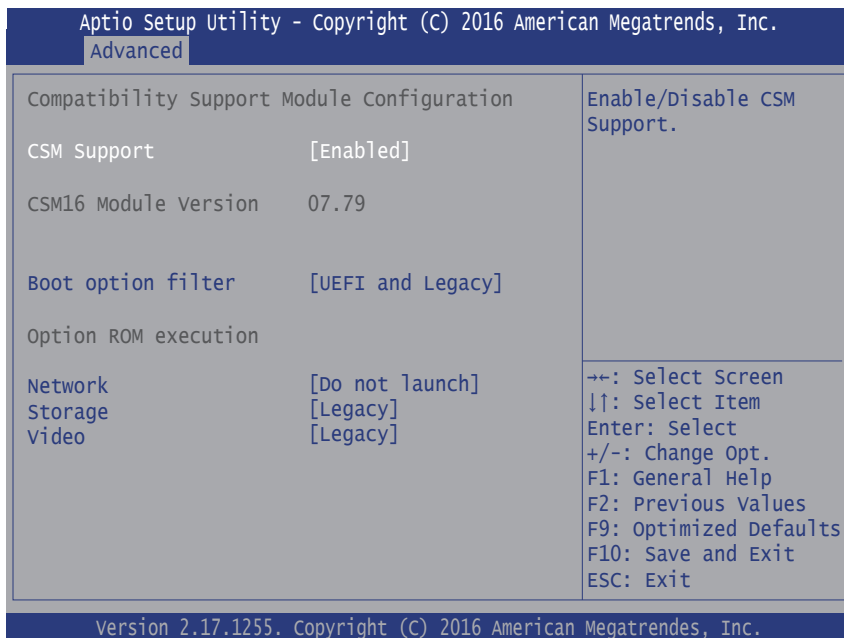
### Parallel Port Configuration

Setting	Description
Parallel Port	<b>Enable</b> (default) or <b>Disable</b> Parallel Port (LPT/LPTE).
Change Settings	Select an optimal setting for Super IO device. <ul style="list-style-type: none"> <li>▶ Options:           <ul style="list-style-type: none"> <li><b>Auto</b></li> <li><b>IO=378h; IRQ=7</b> (default)</li> <li><b>IO=378h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=278h; IRQ=7, 10, 11, 12</b></li> <li><b>IO=3BCh; IRQ=7, 10, 11, 12</b></li> </ul> </li> </ul>
Device Mode (only for Parallel Port Configuration)	Change the Printer Port mode. <ul style="list-style-type: none"> <li>▶ Options:           <ul style="list-style-type: none"> <li><b>STD Printer Mode</b> (default)</li> <li><b>SPP Mode</b></li> <li><b>EPP-1.9 and SPP Mode</b></li> <li><b>EPP-1.7 and SPP Mode</b></li> <li><b>ECP Mode</b></li> <li><b>ECP and EPP 1.9 Mode</b></li> <li><b>ECP and EPP 1.7 Mode.</b></li> </ul> </li> </ul>

### 4.2.9 S5 RTC Wake Settings

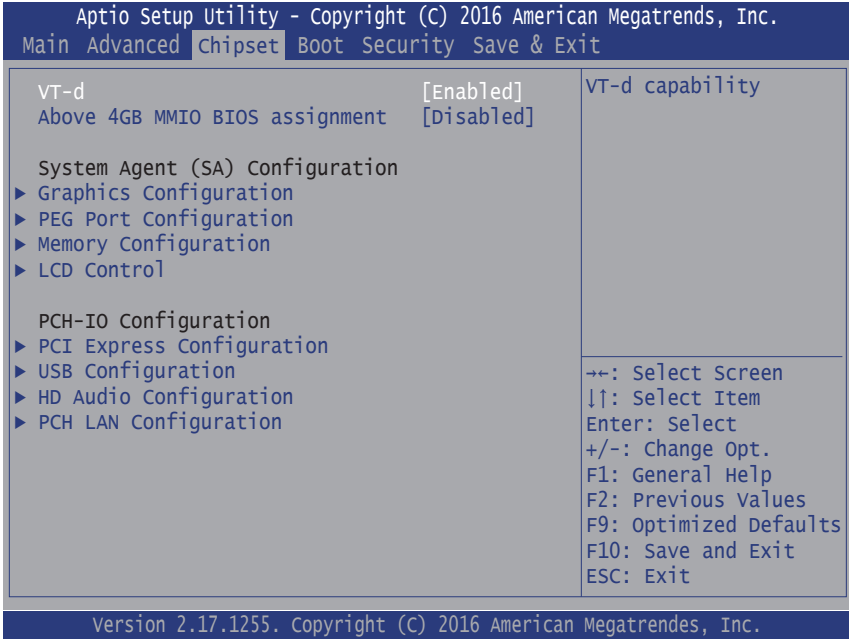
Setting	Description
Wake System from S5	<b>Enable</b> or <b>Disable</b> (default) system wake on alarm event. <ul style="list-style-type: none"> <li>▶ Options available are:           <ul style="list-style-type: none"> <li><b>Disabled</b> (default):</li> <li><b>Fixed Time:</b> System will wake on the hr::min::sec specifiedc.</li> <li><b>DynamicTime:</b> If selected, you need to set <b>Wake up minute increase</b> from 1 - 5. System will wake on the current time + increase minute(s).</li> </ul> </li> </ul>

## 4.2.10 CSM Configuration



Setting	Description
CSM Support	<b>Enable</b> (default) or <b>Disable</b> CSM Support.
Boot option filter	Control the Legacy/UEFI ROMs priority. ▶ Options: <b>UEFI and Legacy</b> (default), <b>Legacy only</b> , <b>UEFI only</b>
Network	Control the execution of UEFI and Legacy PXE OpROM ▶ Options: <b>Do not launch</b> (default) and <b>Legacy</b>
Storage	Control the execution of UEFI and Legacy Storage OpROM ▶ Options: <b>Do not launch</b> and <b>Legacy</b> (default)
Video	Control the execution of UEFI and Legacy Video OpROM ▶ Options: <b>UEFI and Legacy</b> (default)

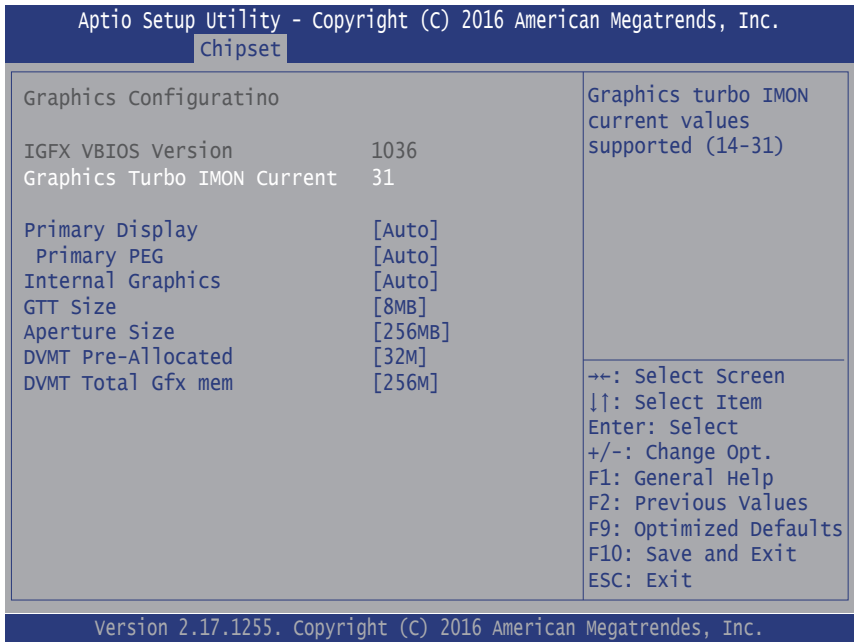
### 4.3 Chipset



Setting	Description
VT-d	<b>Enable</b> (default) or <b>Disable</b> VT-d function
Above 4GB MMIO BIOS assignment	<b>Enable</b> or <b>Disable</b> (default) Above 4GB MMIO BIOS assignment
System Agent (SA) Configuration	
Graphics Configuration	See section <a href="#">4.3.1 Graphics Configuration on page 38</a>
PEG Port Configuration	See section <a href="#">4.3.2 PEG Port Configuration on page 40</a>
Memory Configuration	See section <a href="#">4.3.3 Memory Configuration on page 41</a>
LCD Control	See section <a href="#">4.3.4 LCD Control on page 42</a>

PCH-IO Configuration	
PCI Express Configuration	See section <a href="#">4.3.5 PCI Express Configuration on page 43</a>
USB Configuration	See section <a href="#">4.3.6 USB Configuration on page 44</a>
HD Audio Configuration	Control Detection of the HD-Audio device. ▶ Options available are: <b>Disabled:</b> HDA will be unconditionally disabled <b>Enabled:</b> HDA will be unconditionally Enabled <b>Auto</b> (default) = HDA will be enabled if present, disabled otherwise.
PCH LAN Controller	Enables/Disables onboard NIC. ▶ Options: <b>Enabled</b> (default) and <b>Disabled</b> If enabled, "Wake on LAN" option will be available to <b>Enable</b> (default) / <b>Disable</b> integrated LAN to wake the system. (the Wake On LAN cannot be disabled if ME is on at Sx state.)

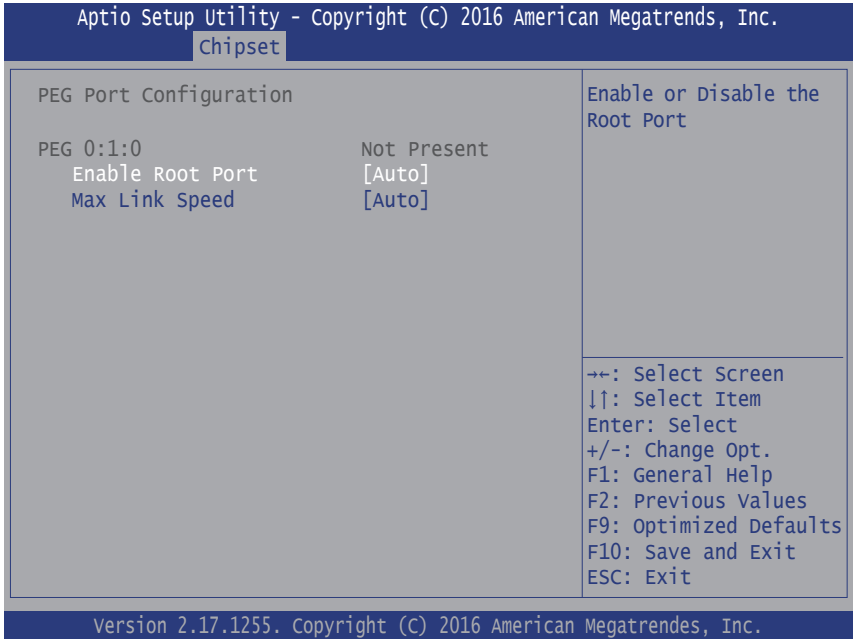
### 4.3.1 Graphics Configuration



Setting	Description
Graphics Turbo IMON Current	Sets the graphics turbo IMON current values. ▶ Options available are <b>14</b> to <b>31</b> (default).
Primary Display	Select which of IGFX/PEG/PCI Graphics device should be Primary Display or select SG for Switchable Gfx. ▶ Options available are <b>Auto</b> (default), IGFX and PEG.
Primary PEG	Set the Primary PEG device. ▶ Options: <b>Auto</b> (default), <b>PEG11</b> , and <b>PEG12</b> .
Internal Graphics	Keep IGD enabled based on the setup options. ▶ Options: <b>Auto</b> (default), <b>Disabled</b> and <b>Enabled</b> .

GTT Size	Select the GTT Size. ▶ Options: <b>4MB</b> , <b>2MB</b> and <b>8MB</b> (default).
Aperture Size	Select the Aperture Size. Note that above 4GB MMIO BIOS assignment is automatically enabled when selecting 2048MB aperture. To use this feature, please disable CSM support. ▶ Options: <b>128MB</b> , <b>256MB</b> (default), <b>512MB</b> , <b>1024MB</b> , <b>2048MB</b> and <b>4096MB</b> .
DVMT Pre-Allocated	Select the DVMT 5.0 Pre-allocated (Fixed) Graphic Memory size used by the Internal Graphic Device. ▶ Options: <b>32M</b> is the default.
DVMT total Gfx Mem	Select the DVMT 5.0 Total Graphic Memory size used by the Internal Graphic Device. ▶ Options: <b>128MB</b> , <b>256MB</b> (default) and <b>Max</b> .

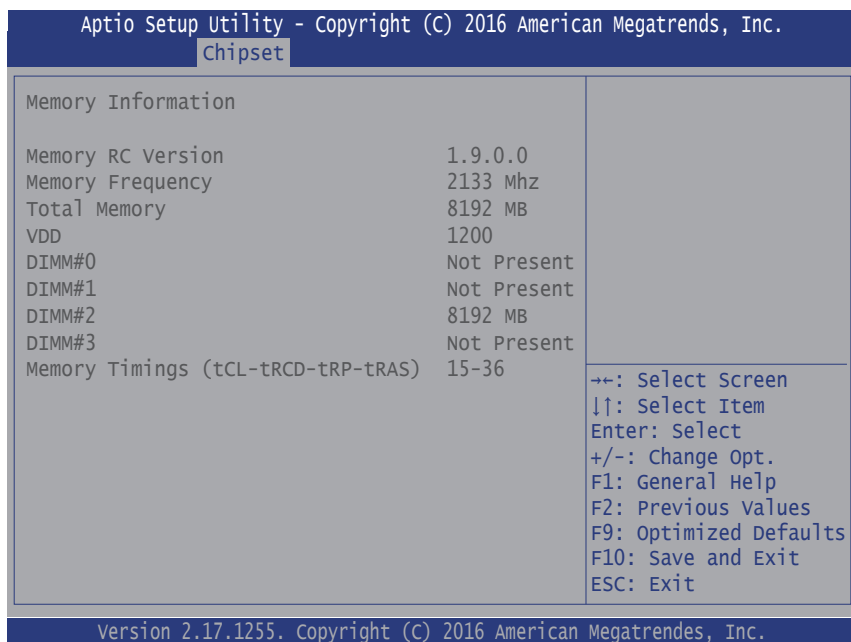
### 4.3.2 PEG Port Configuration



Setting	Description
Enable Root Port	<b>Enable</b> or <b>Disable</b> (default) the Root Port.
Max Link Speed	Configures PEG 0:1:0 Max Speed. ▶ Options: <b>Auto</b> (default), <b>Gen1</b> , <b>Gen2</b> and <b>Gen3</b> .

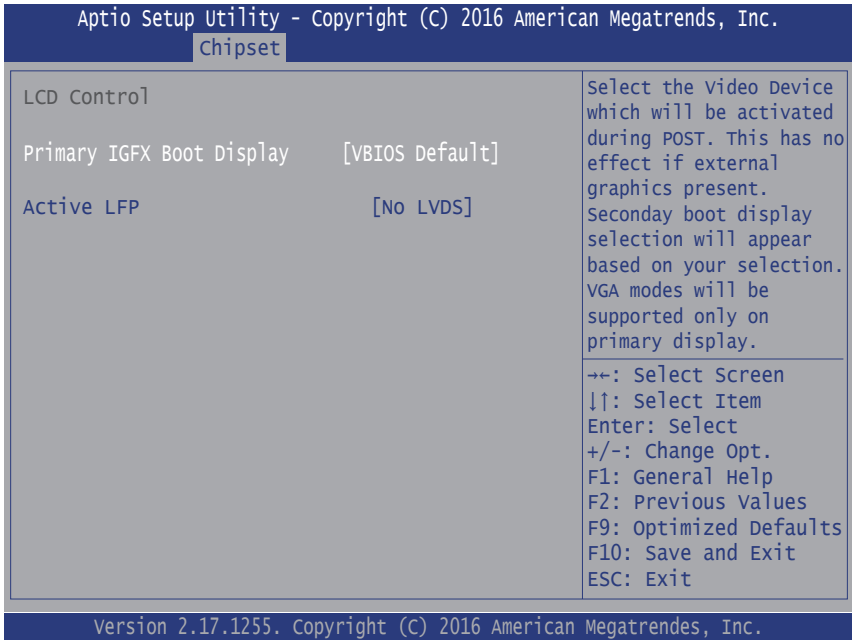


### 4.3.3 Memory Configuration



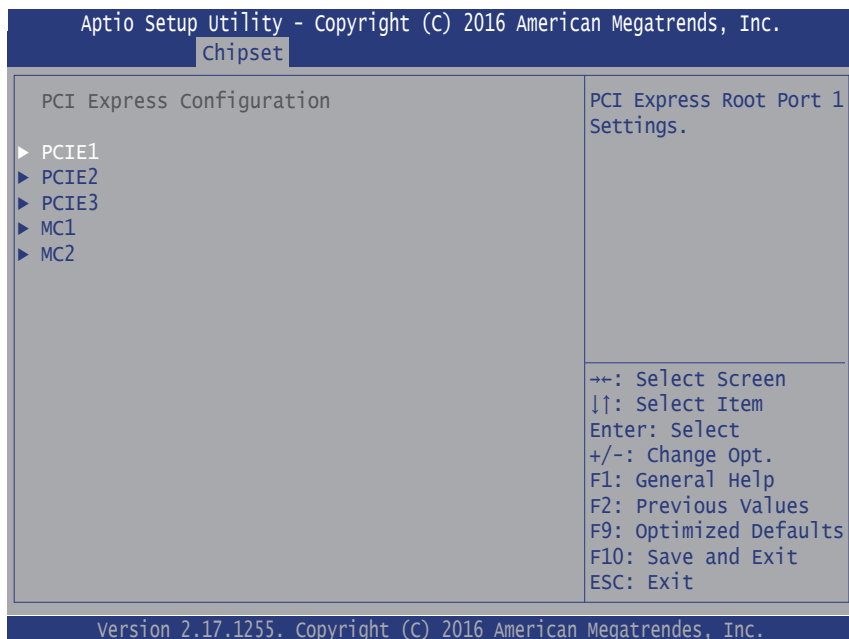
Access this submenu to view the memory configuration.

### 4.3.4 LCD Control



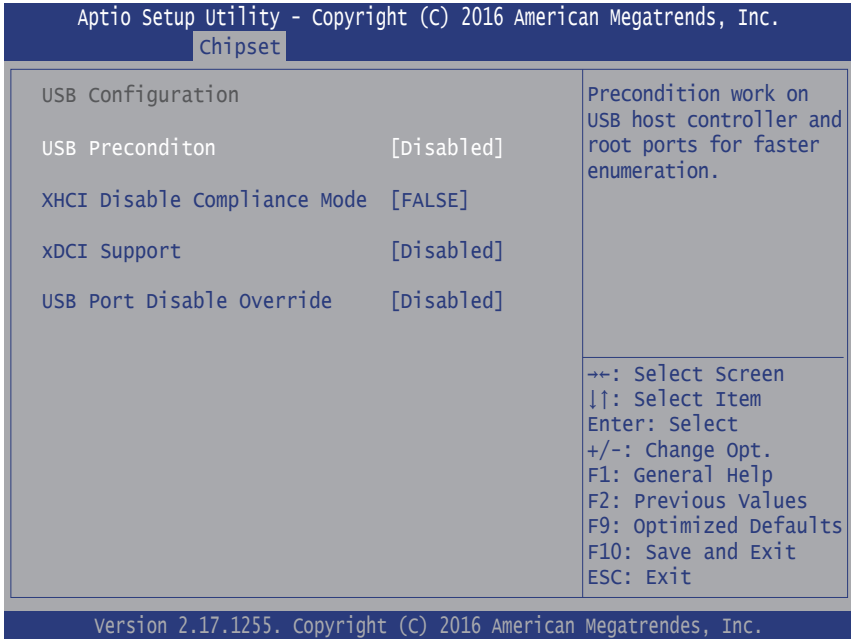
Setting	Description
Primary IGFX Boot Display	Select the Video Device which will be activated during POST. This has no effect if external graphics present. Secondary boot display selection will appear based on your selection. VGA modes will be supported only on primary display. ▶ Options: <b>VBIOS Default</b> (default), <b>LFP</b> , <b>DP1</b> , <b>DVI</b> and <b>DP2</b> .
Active LFP	Configuring LFP usage ▶ Options: <b>No LVDS</b> (default), and <b>eDP Port-A</b>

### 4.3.5 PCI Express Configuration



Setting	Description
PCI Express Configuration & MC1/2	<b>Enable</b> (default) or disable PCI Express Configuration and MC1/2.
ASPM Support	Disable or set the ASPM level. Force L0s will force all links to L0s state. "Auto" will allow BIOS to auto configure."Disable" will disable ASPM. <ul style="list-style-type: none"> <li>▶ Options: <b>Disabled</b> (default), <b>L0s</b>, <b>L1</b>, <b>L0sL1</b> and <b>Auto</b>.</li> </ul>
PCI Express Speed	Select PCI Express port speed. <ul style="list-style-type: none"> <li>▶ Options: <b>Auto</b> (default), <b>Gen1</b>, <b>Gen2</b> and <b>Gen3</b></li> </ul>

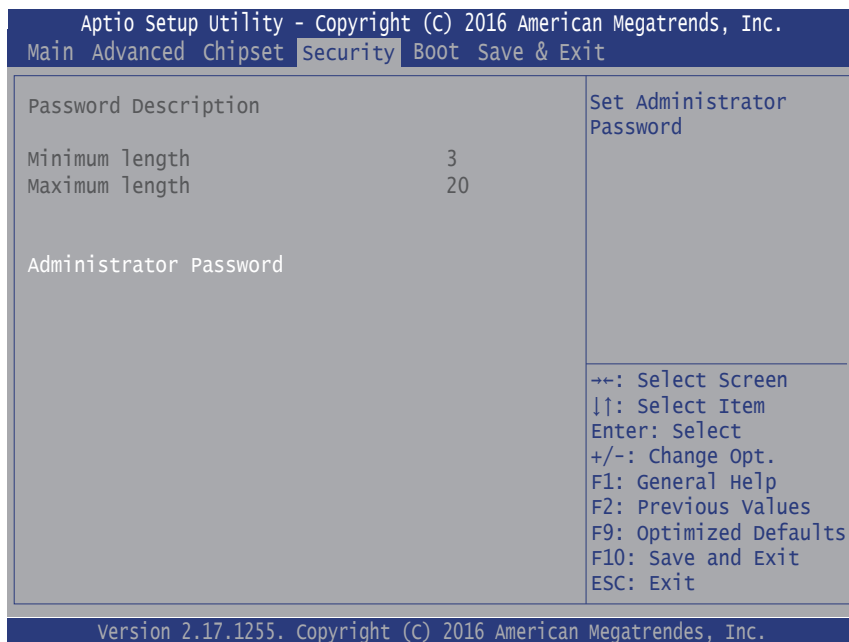
### 4.3.6 USB Configuration



Setting	Description
USB Precondition	Precondition work on USB host controller and root ports for faster enumeration. ► Options: <b>Enable/Disable</b> (default).
XHCI Disable Compliance Mode	Options to disable Compliance Mode. Default is FALSE (default) to not disable Compliance Mode. Set TRUE to disable Compliance Mode.
xDCI Support	<b>Enable/disable</b> (default) xDCI (USB OTG Device).
USB Port Disable Override	Selectively <b>enable/disable</b> (default) the corresponding USB port from reporting a Device Connection to the controller.

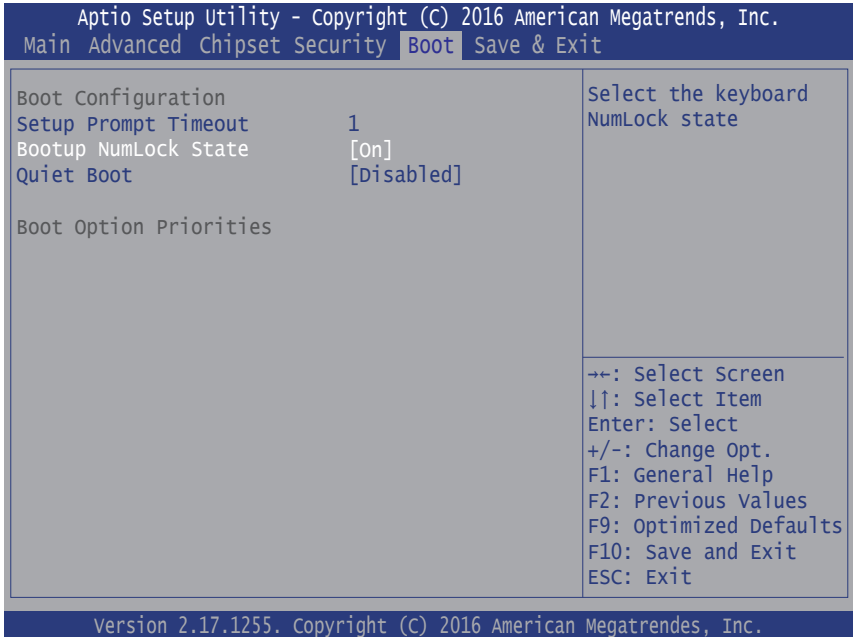
## 4.4 Security

The **Security** menu sets up the administrator password.



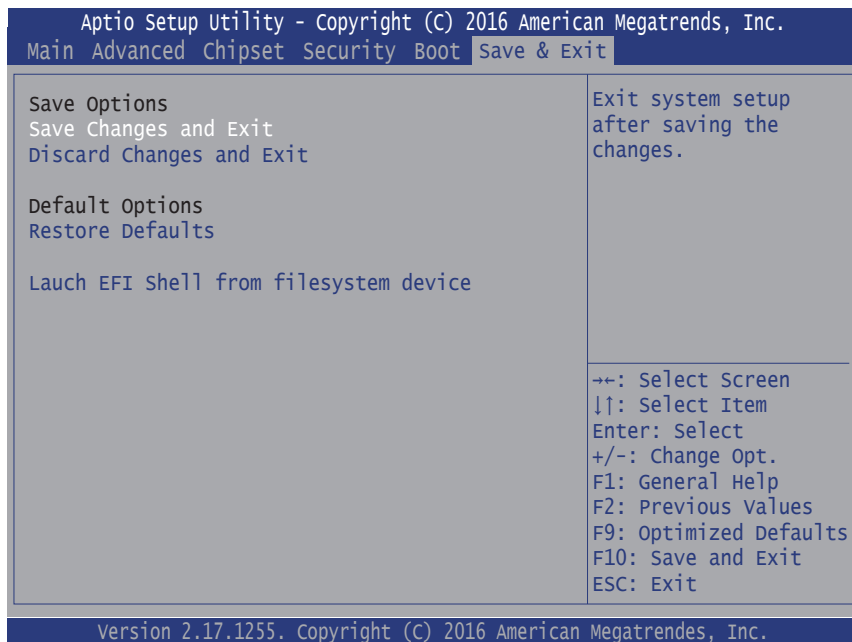
Setting	Description
Administrator Password	<p>To set up an administrator password:</p> <ol style="list-style-type: none"> <li>1. Select <b>Administrator Password</b>. The screen then pops up an <b>Create New Password</b> dialog.</li> <li>2. Enter your desired password that is no less than 3 characters and no more than 20 characters.</li> <li>3. Hit [Enter] key to submit.</li> </ol>

## 4.5 Boot



Setting	Description
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting.
Boot NumLock State	Select the keyboard NumLock state. ▶ Options: <b>On</b> (default) and <b>Off</b> .
Quiet Boot	<b>Enable</b> or <b>Disable</b> (default) Quiet Boot option.

## 4.6 Save & Exit



Setting	Description
Save Changes and Exit	Exit system setup after saving the changes. ▶ Enter the item and then a dialog box pops up: <b>Save configuration and exit? (Yes/ No)</b>
Discard Changes and Exit	Exit system setup without saving the changes. ▶ Enter the item and then a dialog box pops up: <b>Quit without saving? (Yes/ No)</b>
Restore Defaults	Restore/Load Default values for all the setup options. ▶ Enter the item and then a dialog box pops up: <b>Load Optimized Defaults? (Yes/ No)</b>
Launch EFI Shell from filesystem device	Attempts to launch EFI shell application (Shell.efi) from one of the available filesystem devices.



# Appendix



## Appendix A: I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses which also becomes the identity of the device. The following table lists the I/O port addresses used.

Address	Device Description
03F8-03FF	Communications Port (COM1)
02F8-02FF	Communications Port (COM2)
03E8-03EF	Communications Port (COM3)
02E8-02EF	Communications Port (COM4)
002E-002F	Motherboard resources
004E-004F	Motherboard resources
0061-0061	Motherboard resources
0070-0071	Motherboard resources
0080-0080	Motherboard resources
0092-0092	Motherboard resources
00B2-00B3	Motherboard resources
1800-18FE	Motherboard resources
0A00-0A1F	Motherboard resources
0A20-0A2F	Motherboard resources
0A30-0A3F	Motherboard resources
0CF8-0CFF	PCI Express Configuration Access Port
0378-037F	Printer Port (LPT1)
0020-0021	Programmable interrupt controller
00A0-00A1	Programmable interrupt controller
04D0-04D1	Programmable interrupt controller
F040-F05F	SM Bus Controller
F090-F097	Standard SATA AHCI Controller
F080-F083	Standard SATA AHCI Controller
F060-F07F	Standard SATA AHCI Controller
0040-0043	System timer

## Appendix B: BIOS Memory Mapping

Address	Device Description
0xDF000000-0xDF01FFFF	Ethernet Controller
0xDF040000-0xDF043FFF	High Definition Audio Controller
0xDF020000-0xDF02FFFF	High Definition Audio Controller
0xFED00000-0xFED003FF	High Precision Event Timer
0xFF000000-0xFFFFFFFF	Intel(R) 82802 Firmware Hub Device
0xDF030000-0xDF03FFFF	Intel(R) USB 3.0 eXtensible Host Controller - 0100 (Microsoft)
0xA0000-0xBFFFF	Microsoft Basic Display Adapter
0xDE000000-0xDEFFFFFF	Microsoft Basic Display Adapter
0xC0000000-0xCFFFFFFF	Microsoft Basic Display Adapter
0xFED10000-0xFED17FFF	Motherboard resources
0xFED18000-0xFED18FFF	Motherboard resources
0xFED19000-0xFED19FFF	Motherboard resources
0xE0000000-0xEFFFFFFF	Motherboard resources
0xFED20000-0xFED3FFFF	Motherboard resources
0xFED90000-0xFED93FFF	Motherboard resources
0xFED45000-0xFED8FFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Motherboard resources
0xFEE00000-0xFEEFFFFFFF	Motherboard resources
0xDFFE0000-0xDFFFFFFF	Motherboard resources
0xFE029000-0xFE029FFF	Motherboard resources
0xFE028000-0xFE028FFF	Motherboard resources
0xFDAF0000-0xFDAFFFFFFF	Motherboard resources
0xFDAE0000-0xFDAEFFFF	Motherboard resources
0xFDAC0000-0xFDACFFFF	Motherboard resources
0xFD000000-0xFDABFFFF	Motherboard resources
0xFDAD0000-0xFDADFFFF	Motherboard resources
0xFDB00000-0xFDFFFFFFF	Motherboard resources
0xFE000000-0xFE01FFFF	Motherboard resources

---

0xFE036000-0xFE03BFFF	Motherboard resources
0xFE03D000-0xFE3FFFFFF	Motherboard resources
0xFE410000-0xFE7FFFFFF	Motherboard resources
0xDF051000-0xDF051FFF	PCI Data Acquisition and Signal Processing Controller
0xFD000000-0xFDABFFFF	PCI Express Root Complex
0x90000000-0xDFFFFFFF	PCI Express Root Complex
0xDF044000-0xDF047FFF	PCI Memory Controller
0xDF04B000-0xDF04BFFF	SDA Standard Compliant SD Host Controller
0xDF04A000-0xDF04A0FF	SM Bus Controller
0xDF048000-0xDF049FFF	Standard SATA AHCI Controller
0xDF04E000-0xDF04E0FF	Standard SATA AHCI Controller
0xDF04D000-0xDF04D7FF	Standard SATA AHCI Controller
0xFED40000-0xFED40FFF	Trusted Platform Module 1.2

---

## Appendix C: Interrupt Request Lines (IRQ)

Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on board.

Level	Function
IRQ0	System timer
IRQ1	PS/2 Keyboard
IRQ3	Communications Port (COM2)
IRQ4	Communications Port (COM1)
IRQ5	SM Bus Controller
IRQ8	System CMOS/real time clock
IRQ10	Communications Port (COM4)
IRQ11	Communications Port (COM3)
IRQ12	PS/2 Mouse
IRQ16~IRQ23	PCIe Devices

## Appendix D: Watchdog Timer (WDT) Setting

WDT is widely used for industry application to monitor the activity of CPU. Application software depends on its requirement to trigger WDT with adequate timer setting. Before WDT time out, the functional normal system will reload the WDT. The WDT never time out for a normal system. The WDT will not be reloaded by an abnormal system, then WDT will time out and reset the system automatically to avoid abnormal operation.

This board supports 255 levels watchdog timer by software programming I/O ports. Below are the source codes written in C, please take them as WDT application example.

```
#include <math.h>
#include <stdio.h>
#include <dos.h>

int WDTCount;

int main(void)
{
    unsigned char    iCount;

    printf("WDT Times ( 1 ~ 255 ) : ");
    scanf("%d",&iCount);
    printf("\n");

    WDT_Start(iCount);

    return 0;
}

void WDT_Start(int iCount)
{
    outportb(0x66,0xBA);                /* Enable Watch Dog */
    delay(1000);

    WDTCount = iCount;
    outportb(0x62, WDTCount);          /* Number is Watch Dog Down count number */
    delay(1000);

    outportb(0x62, 0x00);              /* Minute is 1 count unit by minute */
                                        /* Minute is 0 count unit by second */
}

void WDT_Stop(void)
{
    outportb(0x66,0xBB);                /* Disable Watch Dog */
}

void WDT_Clear(void)
{

```

## Appendix

---

```
outportb(0x66, 0xBA);           /* Enable Watch Dog */
delay(1000);

outportb(0x62, WDTCount);      /* Number is Watch Dog Down count number */
delay(1000);

outportb(0x62, 0x00);          /* Minute is 1 count unit by minute */
                                /* Minute is 0 count unit by second */
}
```

## Appendix E: DIO Sample Code

```
#include "math.h"
#include "stdio.h"
#include "dos.h"

int SMB_PORT_AD    = 0xF040;
int SMB_DEVICE_ADD = 0x42;

void GPIOMode(int iMode);
void GPIOData(int iData);
int  GPIOStatus();

int main(void)
{
    int iInput;

    GPIOMode(0xF);
    delay(2000);

    GPIOData(0x0A);
    delay(2000);
    iInput = GPIOStatus();
    printf(" Data : %2x \n",iInput);

    GPIOData(0x05);
    delay(2000);
    iInput = GPIOStatus();
    printf(" Data : %2x \n",iInput);

    return 0;
}

void GPIOMode(int iMode)
{
    SMB_Byte_WRITE(SMB_PORT_AD,SMB_DEVICE_ADD,0x03,iMode);    /* DIO 0 ~ 7 Mode */
}

void GPIOData(int iData)
{
    SMB_Byte_WRITE(SMB_PORT_AD,SMB_DEVICE_ADD,0x01,iData);    /* DIO 0 ~ 7 Data */
}

int GPIOStatus()
{
    int iStatus;

    iStatus = SMB_Byte_READ(SMB_PORT_AD,SMB_DEVICE_ADD,0x00);/* DIO 0 ~ 7 Status*/

    return iStatus;
}
```